



FIGURE 1

The following figures illustrate conceptual details of parts of the overall resource-address-translation flow.

AT	AID=0000	AID=0001	AID=0002		AID=FFFF
00	ART_Example				
01					
02					
FE					
FF					

Adapter-Type / Adapter-ID to Adapter-Resource Table Address

FIG. 2A

RT	ART_Example
00	ART Entry <sub>0</sub>
01	ART Entry <sub>1</sub>
02	ART_Example Entry for CQ Resource Type
03	
FF	ART Entry <sub>255</sub>

Adapter Resource Table (ART)

FIG. 2B

DWord	ART_Example Entry for CQ Resource Type				
0	ASAB		PRC		MRC
1	SAC	Reserved	SMC		Reserved
2	Reserved	LARID		Reserved	RIL
3	Access-Table Address (ATA) - AT is for all CQ Resources				
4	Adapter-Storage Base Address for SAT=0				
5	Adapter-Storage Base Address for SAT=1				
6-18	Reserved				
19	Adapter-Storage Base Address for SAT=15				
Bits	0	8	16	32	40
				62	63

Figure 1C Adapter-Resource-Table Entry (ARTE).

Figure 3 Resource Address Translation.

